

IN THE CLAIMS

Please amend the claims as follows:

1. (previously presented) A method for receiving a wireless signal by a computer adapted to operate in a power-saving mode, said method comprising:

providing a plurality of status bits to indicate whether or not a RF module is attached to said computer and is activated;

detecting within said computer a wireless signal representing a bit sequence when said computer is operating in a power-saving mode, wherein said wireless signal is targeted for said computer;

determining whether said RF module is attached to said computer and is activated by reading said plurality of status bits;

exiting said power-saving mode only if said RF module is attached to said computer and is activated;

regenerating some or all of said bit sequence of said wireless signal; and

storing said some or all of said bit sequence of said wireless signal in a memory after exiting said power-saving mode.

2. cancelled

3. (previously presented) The method of claim 1, wherein said detecting further includes detecting a particular identification tag embedded in said bit sequence.

1 4. (previously presented) The method of claim 1, wherein wireless signal is transmitted
2 through a radio frequency channel.

1 5. (previously presented) The method of claim 1, wherein said bit sequence includes a
2 request for said computer to exit said power-saving mode.

1 6. (previously presented) The method of claim 1, wherein said bit sequence includes a
2 request to resume execution of a program that has been suspended when said computer is in said
3 power-saving mode.

1 7. (currently amended) The method of claim 1, wherein said method further includes setting
2 a field effect transistor (FET) switch to maintain power to a receiving means prior to entering
3 said power-saving mode.

8. cancelled

1 9. (previously presented) The method of claim 1, wherein said method further includes:

2 processing information conveyed by said bit sequence; and

3 automatically returning to said power-saving mode after said processing.

1 10. (presently amended) A computer for receiving a wireless signal while in a power-saving
2 mode, said computer comprising:

3 a receiving means adapted to detect a wireless signal representing a sequence of
4 bits, wherein said receiving means is adapted to regenerate some or all of said bit
5 sequence, wherein said wireless signal is targeted for said computer;

6 a plurality of status bits for indicating whether or not a RF module is attached to
7 said computer and is activated;

8 a power-saving mode control means adapted to exit said power-saving mode only
9 if said plurality of status bits indicate said RF module is attached to said computer and
10 is activated;

11 a field effect transistor (FET) switch for enabling power to said receiving means
12 when said computer is in said power-saving mode; and

13 a memory for storing said some or all of said regenerated bit sequence after said
14 computer has exited said power-saving mode.

11-13. cancelled

1 14. (previously presented) The computer of claim 10, wherein said receiving means is an
2 optional attachment to said computer.

1 15. (previously presented) The computer of claim 10, wherein said receiving means is formed
2 in a device bay cover.

1 16. (previously presented) The computer of claim 15, wherein said device bay cover is an
2 optional attachment to said computer.

17-19. cancelled